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APPLICATION NO.		FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO		
10/736,618		12/17/2003	Yoshihiro Ohkura	X2007.0147	4069		
32172	7590	12/27/2005		EXAM	EXAMINER		
		PIRO MORIN & O	NGUYEN	NGUYEN, DILINH P			
1177 AVE 41 ST FL.	NUE OF 1	THE AMERICAS (61	ART UNIT	PAPER NUMBER			
NEW YOR	K, NY 1	10036-2714	2814				

DATE MAILED: 12/27/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application	on No.	Applicant(s) OHKURA, YOSHIHIRO		
		10/736,61	8			
	Office Action Summary	Examiner		Art Unit		
		DiLinh Ng	<u> </u>	2814		
Period fo	The MAILING DATE of this communication or Reply	n appears on the	cover sheet with the c	orrespondence ad	ldress	
WHIC - Exter after - If NO - Failu Any r	ORTENED STATUTORY PERIOD FOR RICHEVER IS LONGER, FROM THE MAILIN nsions of time may be available under the provisions of 37 CF SIX (6) MONTHS from the mailing date of this communicatio period for reply is specified above, the maximum statutory per to reply within the set or extended period for reply will, by seply received by the Office later than three months after the part of the patent term adjustment. See 37 CFR 1.704(b).	G DATE OF TH FR 1.136(a). In no even n. eriod will apply and wi statute, cause the appl	IIS COMMUNICATION int, however, may a reply be time to the time time to the time time time time time time time tim	. ely filed lhe mailing date of this c 0 (35 U.S.C. § 133).		
Status						
2a)⊠	Responsive to communication(s) filed on go This action is FINAL. 2b) Since this application is in condition for all closed in accordance with the practice under the condition of the condition is in condition.	This action is nowance except	on-final. for formal matters, pro		e merits is	
Dispositi	on of Claims					
5)□ 6)⊠ 7)□	Claim(s) 1 and 3-9 is/are pending in the a 4a) Of the above claim(s) is/are with Claim(s) is/are allowed. Claim(s) 1 and 3-9 is/are rejected. Claim(s) is/are objected to. Claim(s) are subject to restriction a	hdrawn from co				
Applicati	on Papers					
10)	The specification is objected to by the Exa The drawing(s) filed on is/are: a) Applicant may not request that any objection to Replacement drawing sheet(s) including the co The oath or declaration is objected to by the	accepted or b) the drawing(s) borrection is requir	e held in abeyance. See ed if the drawing(s) is ob	e 37 CFR 1.85(a). lected to. See 37 C).
Priority (ınder 35 U.S.C. § 119					
12)⊠ a)	Acknowledgment is made of a claim for for All b) Some * c) None of: 1. Certified copies of the priority docur 2. Certified copies of the priority docur 3. Copies of the certified copies of the application from the International Besee the attached detailed Office action for	ments have bee ments have bee priority docume ureau (PCT Rul	n received. n received in Applicati ents have been receive e 17.2(a)).	on No ed in this National	l Stage	
2) Notice	e of References Cited (PTO-892) se of Draftsperson's Patent Drawing Review (PTO-94) mation Disclosure Statement(s) (PTO-1449 or PTO/S		4) Interview Summary Paper No(s)/Mail Do 5) Notice of Informal F 6) Other:	ate	O-152)	

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.
- 2. Claim 1 is rejected under 35 U.S.C. 102(e) as being anticipated by Ohuchi et al. (U.S. Pat.6495916) (previously applied).

Ohuchi et al. disclose a surface mount chip package incorporating a semiconductor chip having an integrated circuit, comprising:

a package housing 5 made of a resin that covers the semiconductor chip 1 while avoiding a plurality of conductors 4 extending from the semiconductor chip 1;

a plurality of external electrodes 7 that are arranged in the package housing 5 in correspondence with a main surface of the semiconductor chip having the integrated circuit and are connected with the plurality of conductors extending from the semiconductor chip; the external electrodes 7 each having a circular shape when viewed in a thickness direction of the semiconductor chip; and

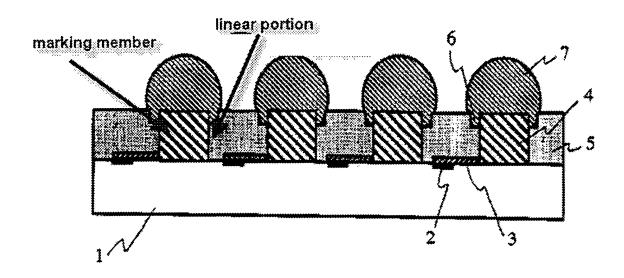
at least one marking member that is arranged in the package housing so as to realize a directivity when viewed in the thickness direction of the semiconductor chip,

wherein an outline shape of the marking member includes at least one linear portion (fig. 1, column 2, lines 56 et seq.).

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Fig.1



Claim Rejections - 35 USC § 103

- 3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 4. Claims 3-9 are rejected under 35 U.S.C. 103(a) as being unpatentable over Ohuchi et al. (U.S. Pat. 6495916) in view of AAPA (fig. 6).

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- Regarding claim 3, Ohuchi et al. disclose that the marking member corresponds to a copper post (fig. 1, column 3, line 1) but do not explicitly disclose the marking member is arranged independently of the plurality of conductors. However, AAPA discloses that the marking member 520 is arranged independently of the plurality of conductors (fig. 6). Therefore, it would have been obvious to one having ordinary skill in the art to modify the device of Ohuchi et al. by having the marking member is arranged independently of the plurality of conductors, as taught by AAPA (fig. 6), in order to provide an index solder ball for use for the chip direction (page 2, lines 21-22).
- Regarding claim 4, Ohuchi et al. disclose that the copper post serving as the marking member has a square shape (fig. 1).
- Regarding claim 5, Ohuchi et al. disclose that the copper post is covered with a solder layer 7 (fig. 1, column 3, line 35).
- Regarding claim 6, Ohuchi et al. disclose that the marking member has a square shape (fig. 1).
- Regarding claims 7-9, Ohuchi et al. disclose that the marking member corresponds to a copper post (column 3, line 1) and it would have been obvious to increase in size compared with each of the plurality of conductors 4 (fig. 1).

Moreover, the semiconductor member is increased in size would have been obvious to an ordinary artisan practicing the invention because, absent evidence of disclosure of criticality for the size giving unexpected results, it is not inventive to discover optimal or workable ranges by routine experimentation. *In re Aller*, 220 F.2d

454, 105 USPQ 233, 235 (CCPA 1955). Furthermore, the specification contains no disclosure of either the critical nature of the claimed dimensions of any unexpected results arising therefrom. Where patentability is aid to be based upon particular chosen dimensions or upon another variable recited in a claim, the Applicant must show that the chosen dimensions are critical. See *In re Woodruff*, 919 F.2d 1575, 1578, 16 USPQ2d 1934, 1936 (Fed, Cir. 1990).

Response to Arguments

Applicant's arguments filed 10/7/05 have been fully considered but they are not persuasive.

The applicant argues that Ohuchi et al. do not disclose the external electrodes each having a circular shape when viewed in a thickness direction of the semiconductor chip and an outline shape of the marking member includes at least one linear portion.

Applicant's arguments have been fully considered but they are not persuasive because Ohuchi et al. clearly disclose the package comprising the external electrodes 7 each having a circular shape when viewed in thickness direction of the semiconductor chip and wherein an outline shape of the marking member includes at least one linear portion (fig. 1, column 2, lines 56 et seq.).

Conclusion

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to DiLinh Nguyen whose telephone number is (571) 272-1712. The examiner can normally be reached on 8:00AM - 6:00PM (M-F).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wael Fahmy can be reached on (571) 272-1705. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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